DHIRAJ BORADE

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OBJECTIVE

Actively seeking Summer Internship / Fall Co-op in order to develop myself into an excellent Computer Engineering professional through continuous learning and research in both technical and managerial aspects of business.

EDUCATION

Master of Science: Computer Science (GPA - 3.2), Expected Graduation - May 2018

***University of Florida*** - *Gainesville, Florida*, *USA*

Coursework in Distributed Operating Systems Principles, Computer Architecture Principles, Concurrent Programming,  
Analysis of Algorithms, Database Management Systems and Advance Data Structures.

Bachelor of Engineering: Electronics Engineering (GPA - 3.3), Graduation - May 2013

***University of Mumbai*** - *Mumbai, Maharashtra*, *India*

Coursework in Embedded Systems, Real Time Operating Systems, Advance VLSI Design, Neural Networks and Fuzzy  
Systems, Digital Image Processing Design and Discrete and Continuous Time Signal and Systems.

Licentiate in Engineering: Electronics Engineering (GPA - 3.85), Graduation - May 2010

***Veermata Jijabai Technological Institute*** - *Mumbai, Maharashtra*, *India*

EXPERIENCE

Senior Design Engineer (July 2013 to June 2016)

***Larsen and Toubro Limited*** – *Mysore, Karnataka*, *India*

Designed and developed Firmware/Software and Hardware for Single Phase Energy Meter on various platforms viz.,  
Texas Instruments, Renesas and Analog Devices.  
Developed an Android and iOS Application using Android Studio (JAVA) and Xcode (SWIFT) as an Interface to  
generate Energy Bill by acquiring data from Energy Meter directly to Mobile Phone and also pushed onto the Cloud, to  
be collected by the Electricity Utility.  
Formulated and analyzed Algorithms during the development of Real Time Operating System of the Energy Meter.  
Customized various communication modules viz., IrDA (Infrared Data Association), Bluetooth Chip and Low Power  
Communication Modules from Cyan Technologies for communicating with the Single Phase Energy Meter.  
Project Life-Cycle Management of Single Phase Energy Meter.  
Designed circuits and modules and drafted PCB using CAD/CAM tools for achieving immunity to EMI/EMC i.e.  
Electromagnetic Interference.

Trainee Engineer - Full Time Internship (November 2008 to November 2009)

***APLAB Limited*** – *Mumbai, Maharashtra*, *India*

Research and Development of Single and Three Phase Online Uninterruptible Power Supply (UPS) Systems, Frequency  
Converters, Static Transfer Switch, Inverters and Isolation Transfer.

SKILLS

Proficient in Embedded C, C/C++, C#, Java, JavaScript, Experience in IAR Systems for Texas Instruments and  
Python, PERL, SCALA, Labview, Objective-C, Swift, Analog Devices, Cubesuite+ for Renesas, Android  
SQL, Data Structures and Database. Studio, Xcode, Eclipse and NetBeans.  
Operating Systems: Linux/Unix, Mac OS X and CAD/CAM tools for Printed Circuit Boards.  
Microsoft Windows and Tools. Experience in PSpice simulator and Ladder  
VHSIC Hardware Description Language using ISE programming for Programmable Logic Controller.  
Simulator from Xilinx, Advance VLSI Design. Experience in Cloud and Mobile Computing, Operating  
Web Development Tools: CSS, PHP and HTML. Systems and Internet of Things.

ACADEMIC PROJECTS

Implemented MIPS32 Simulator using Tomasulo Algorithm with Branch Prediction in JAVA – December 2016  
Wireless Home Automation using IoT (Internet of Things) – December 2016  
Implemented 8255A PPI (Programmable Peripheral Interface) on FPGA using VHDL - May 2012

ACHIEVEMENTS

GROUND BREAKER INNOVATION AWARD during the Unit Level Awards 2014-15 of Larsen & Toubro Limited, India  
2nd Prize in National Level Second PG Symposium for Computer Engineering (CPGCON) 2013 for paper presentation.  
1st Prize in National Level Debate Competition organized by SV Patel Institute of Technology, India (February 2009).